

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Masato MORI et al. Group Art Unit : 1793

Appl. No. : 10/597,949 (U.S. National Examiner : Devang R. PATEL  
Stage of PCT/JP2005/003043)

I.A. Filed : February 24, 2005 Confirmation No. : 8311

For : ELECTRONIC COMPONENT MOUNTING METHOD, AND CIRCUIT  
SUBSTRATE AND CIRCUIT SUBSTRATE UNIT USED IN THE  
METHOD

**ELECTION**

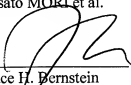
Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop AMENDMENT  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

In response to the Restriction Requirement dated October 10, 2008, Applicants hereby elect  
Group I, comprising claims 1-14.

Should the Examiner have any questions, the Examiner is invited to contact the  
undersigned at the below-listed telephone number.

Respectfully submitted,  
Masato MORI et al.

  
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November 10, 2008  
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